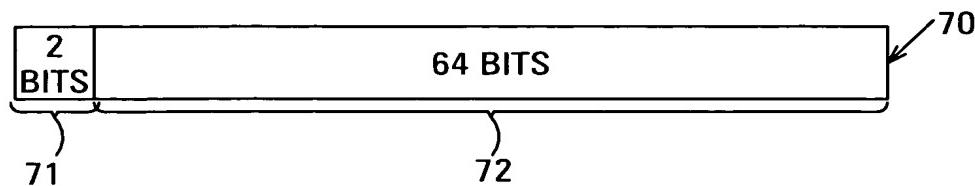
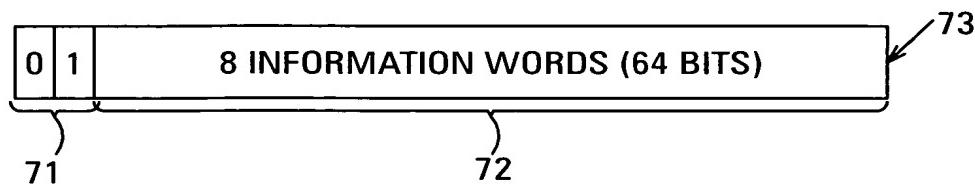


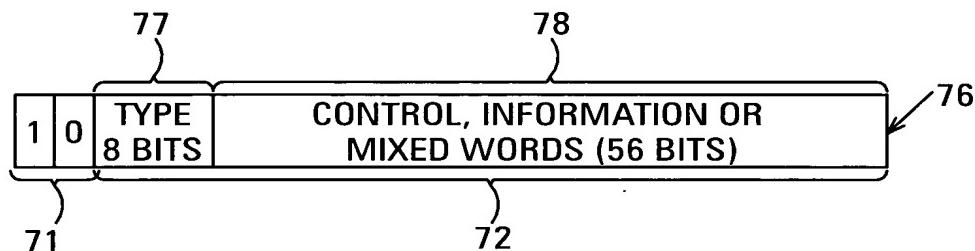
**FIG. 1**  
(PRIOR ART)



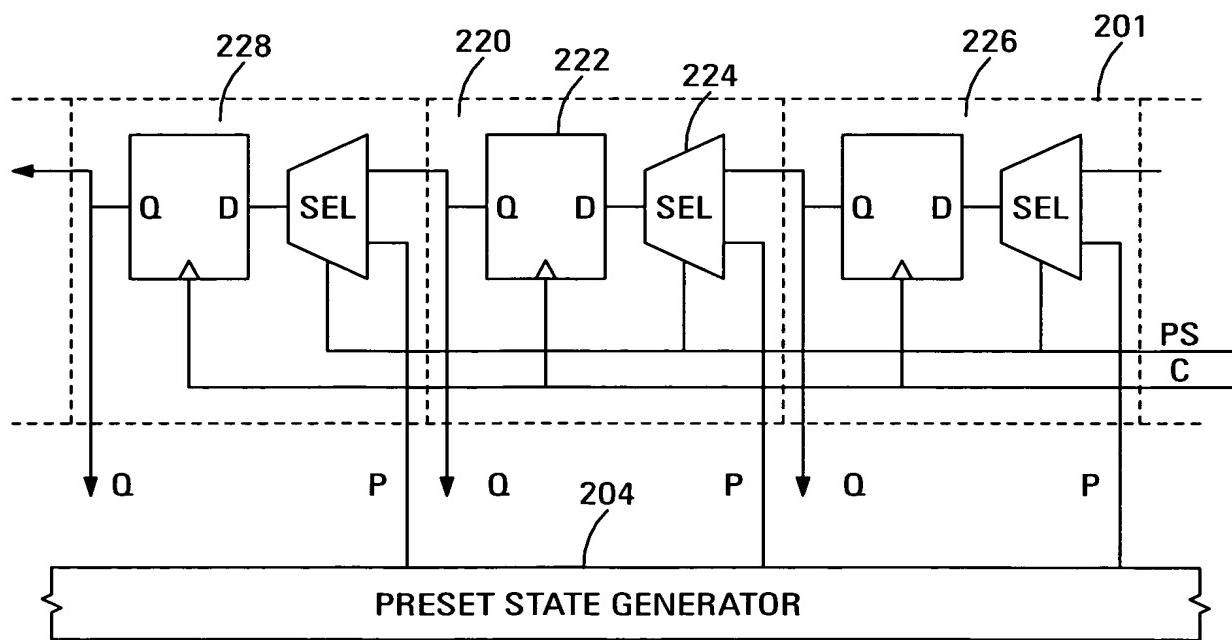
**FIG. 2A**  
 (PRIOR ART)



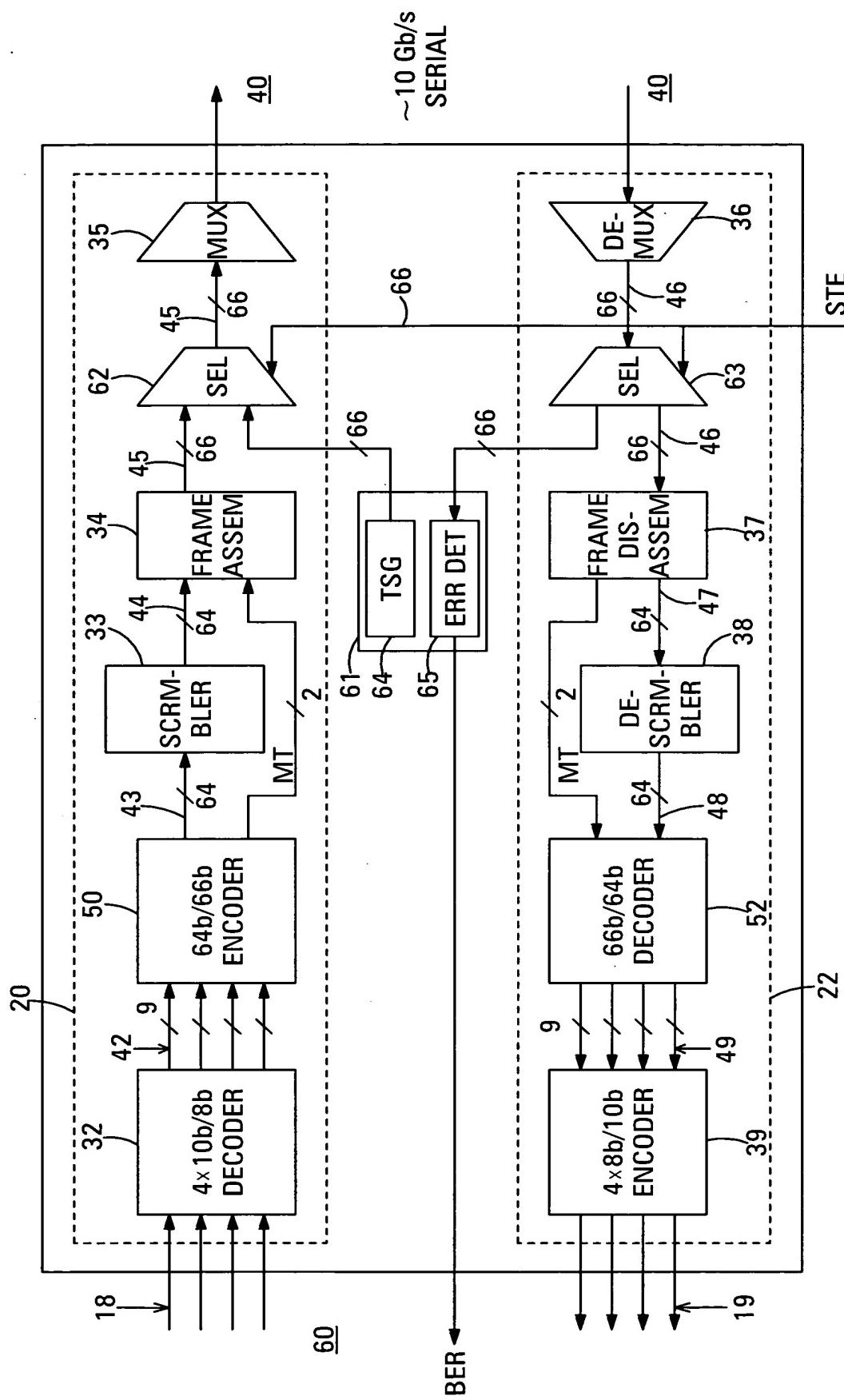
**FIG. 2B**  
 (PRIOR ART)



**FIG. 2C**  
 (PRIOR ART)



**FIG. 6**



**FIG.3**  
 (PRIOR ART)

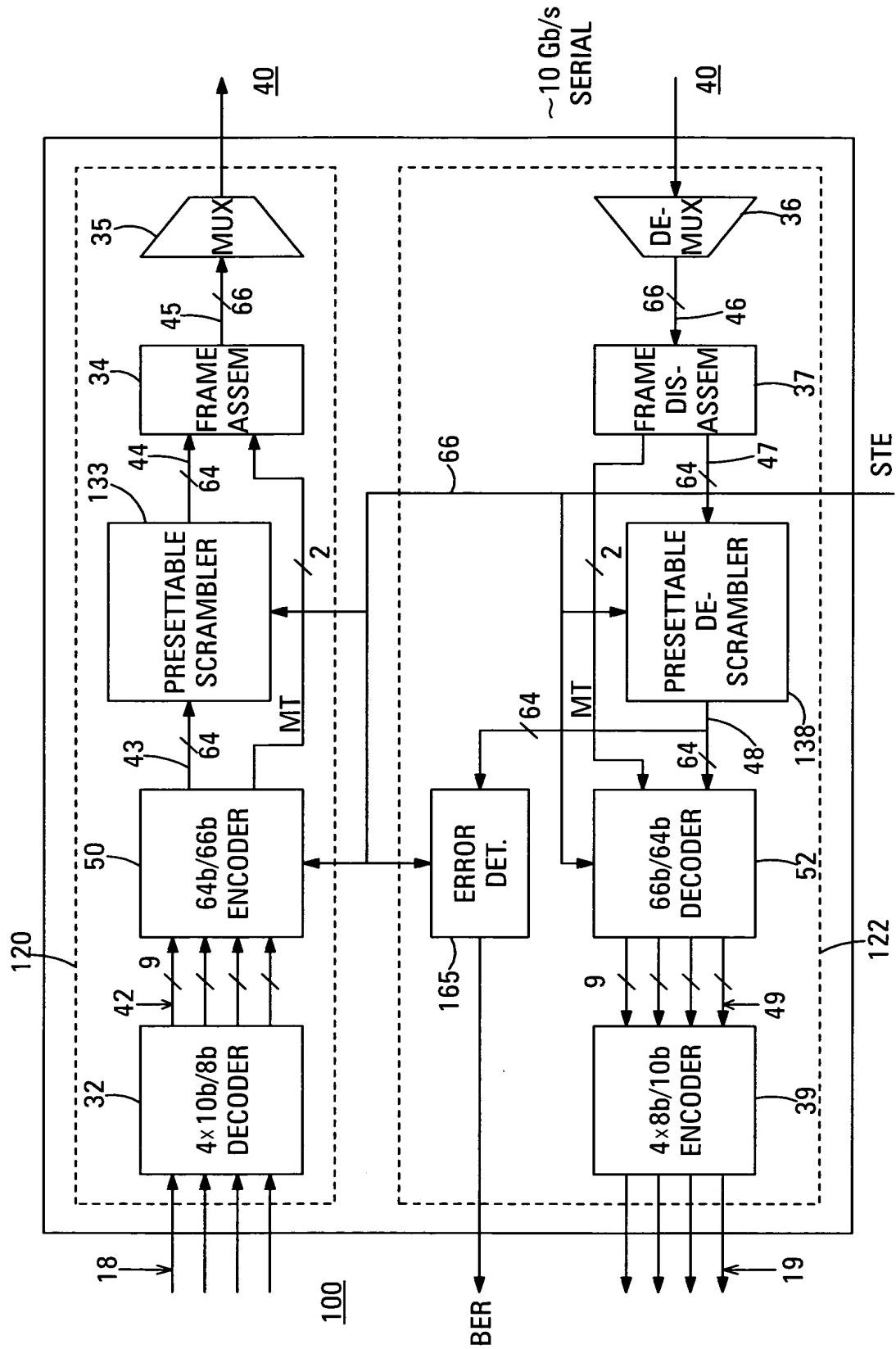
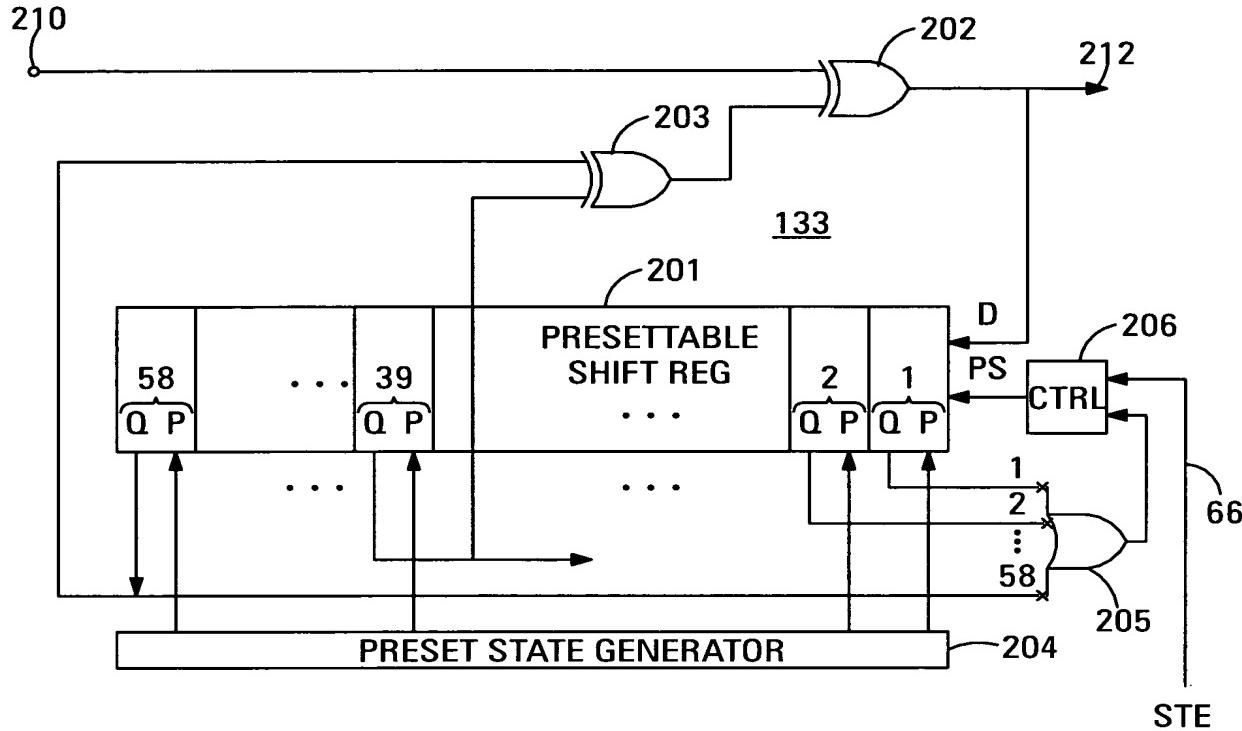
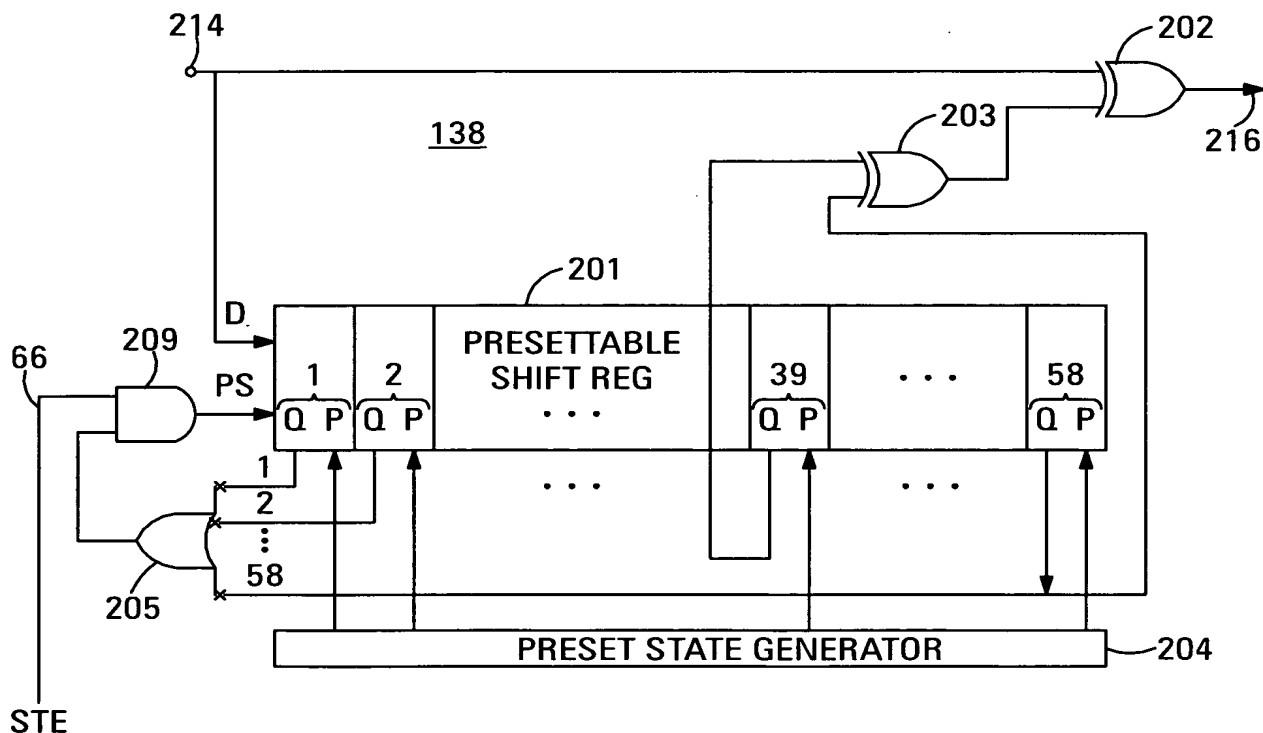


FIG. 4



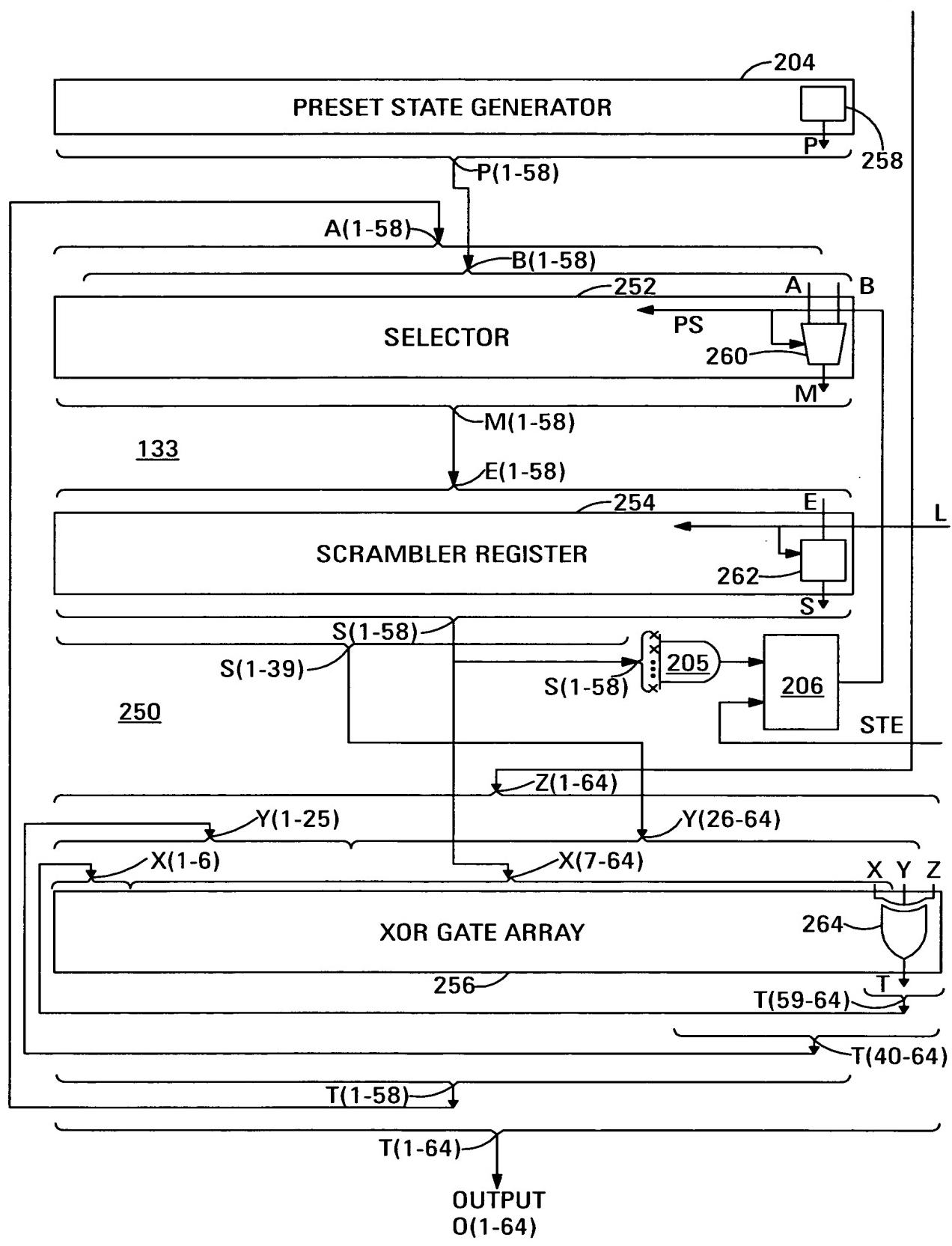
**FIG.5A**



**FIG.5B**



INPUT  
 I(1-64)



**FIG.7**